

SEP 21 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket: DE010005

Bretschneider, Ernst et al.

Serial No.: 10/056,373

Art Unit: 2859

Filed: January 17, 2002

Examiner: Reis, Travis M.

Title: Circuit Arrangement And Method of Protecting At Least A Chip Arrangement From Manipulation And/Or Abuse


Assistant Commissioner for Patents  
Alexandria, VA 22313-1450

## EXTENSION OF TIME STATEMENT

The Commissioner is hereby requested and authorized pursuant to 37 CFR §1.136(a)(3), to treat any concurrent or future reply in this application requiring a petition for extension of time for its timely submission, as incorporating a petition for extension of time for the appropriate length of time. Please charge any additional fees which may now or in the future be required in this application, including extension of time fees, but excluding the issue fee unless explicitly requested to do so, and credit any overpayment, to Deposit Account No. 14-1270.

Respectfully submitted,

By

  
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September 21, 2005

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